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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/656,504	09/07/2000	Peter Krause	004860.P2449	2231

7590 06/10/2003

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EXAMINER

NIEVES, MICHAEL A

ART UNIT	PAPER NUMBER
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2185

DATE MAILED: 06/10/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/656,504

Applicant(s)

KRAUSE ET AL.

Examiner

Michael A Nieves

Art Unit

2185

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 07 September 2000.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 September 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Fujii US Patent No. 6,140,714.

3. As per claims 1 and 9, Fujii discloses a system comprising:

A first stage associated with a first component of the digital processing system

[Figure 1, reference no. 11, primary power-supply circuit];

A second stage associated with a second component of the digital processing

system [Figure 1, reference no. 12, secondary power-supply circuit], said stage

coupled to said first stage [Figure 1, primary power-supply circuit is electrically

coupled to secondary power-supply circuit];

Wherein said first stage drives said second stage [column 4, lines 60-65].

4. As per claim 2, Fujii discloses first and second stages are separated from each other [Figure 1, primary power-supply circuit is separated from the secondary power-supply circuit].
5. As per claim 4, see Figure 4.
6. As per claims 5 and 13, Fujii discloses said first component comprises a display device [Figure 1, reference no. 21, first driven circuit; column 1, lines 14-19, first driven circuit is a functional element] and said second component comprises a microprocessor [Figure 1, reference no. 22, second driven circuit; column 1, lines 14-19, second driven circuit is a functional element].
7. As per claims 6 and 12, Fujii discloses first stage and second stage are located proximately to said display device and said microprocessor [Figure 1].
8. As per claim 7, Fujii discloses said first stage provides power for said first component and said second stage provides power for said second component [Figure 1; column 4, lines 60-65].
9. As per claims 8, 14, and 20, see column 4, lines 45-59 [both power supply circuits are dc-dc converters].

10. As per claims 3, 10, and 19, Fujii discloses said first and second circuits are electrically coupled to each other such that first circuit can drive said second circuit [Figure 1; column 4, lines 60-65].
11. As per claim 11, it is inherent that said second circuit and said second component are disposed on a printed circuit board [Figure 1].
12. As per claim 15, Fuji discloses a power supply circuit coupled to at least a display device and a microprocessor of the computer system [Figure 1, reference no. 21 and 22, first and second driven circuits; column 1, lines 14-19, driven circuits are functional elements and power is supplied to these elements], wherein said power supply is capable of supplying power to said display device and said microprocessor using at least two distinct power supply stages [Figure 1, reference no. 11 and 12, primary and secondary power supply circuits; column 9, lines 36-43].
13. As per claims 16 and 17, Fujii discloses a main circuit coupled to one of said display device and said microprocessor [Figure 1; column 9, lines 36-38, primary power-supply circuit is coupled to first driven circuit; Figure 1, reference no. 21, first driven circuit; column 1, lines 14-19, first driven circuit is a functional element] and a secondary circuit coupled to the other of said display device and said microprocessor [Figure 1; column 9,

Art Unit: 2185

lines 39-43, secondary power-supply circuit is coupled to second driven circuit]; wherein said main circuit drives said secondary circuit [column 4, lines 60-65].

14. As per claim 18, Fujii discloses said main circuit and said secondary circuit are physically isolated from each other [Figure 1].


Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael A Nieves whose telephone number is (703) 305-7583. The examiner can normally be reached on m-f 8:00 AM - 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thomas Lee can be reached on (703) 305-9717. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 305-5404 for regular communications and (703) 304-5404 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Michael Nieves
June 2, 2003



THOMAS LEE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100